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CONFIRMATION NO. 8716

SERIAL NUMBER 10/501,826	FILING OR 371(c) DATE 07/19/2004 RULE	CLASS 257	GROUP ART UNIT 2811	ATTORNEY DOCKET NO. IN 020001
APPLICANTS Pradip Mandal, Bangalore, INDIA;				
** CONTINUING DATA ***** This application is a 371 of PCT/IB02/05484 12/12/2002				
** FOREIGN APPLICATIONS ***** EUROPEAN PATENT OFFICE (EPO) 02075258.0 01/22/2002				
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance		STATE OR COUNTRY INDIA	SHEETS DRAWING 1	TOTAL CLAIMS 8
Verified and Acknowledged Examiner's Signature _____ Initials _____				INDEPENDENT CLAIMS 1
ADDRESS 24737				
TITLE Voltage limiting semiconductor pass gate circuit				
FILING FEE RECEIVED 920	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	